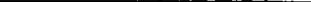


<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p>Complete If Known</p>	
Sheet	1	of	2	Application Number	10/666,482
				Filing Date	September 18, 2003
				First Named Inventor	Koji Yamada
				Art Unit	2883
				Examiner Name	ERIN CHIEM
				Attorney Docket Number	96790P442

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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				Examiner Name	ERIN CHIEM
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
eBc		"Optical Interconnecting Structure of SI Waveguide on SOI Substrate"; T. Shoji, et al., NTT Telecommunications Energy Labs, one page (w/English abstract)	

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